

L10	10	smart adj card same decay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 14:50
L11	36	smart adj card same time adj limit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:04
L12	288	timing adj circuit same expiration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:29
L13	330	time adj limit adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:49
L14	0	13 same smart adj card	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:32
L15	11	13 same (activat\$3 deactivat\$3 disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:35
L16	73	13 same (start stop)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:36
L17	7	16 same function	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:36
L18	9	13 same lapse	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:56
L19	105	smart adj card same delay same time	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:56

L20	2	smart adj card same delay same time adj limit\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/13 15:56
-----	---	--	---	----	----	------------------